## OCT 1 4 2005

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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fe application of: Boerstler et al.

Serial Number: 10/692,415

Filed: October 23, 2003

For: DEFEATABLE ESD FOR IMPROVING

I/O PIN BANDWIDTH

Group Art Unit: 2836

Examiner: Ronald Leja

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

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On: 10/10/05

Abhley Reifer

## SECOND RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action dated August 10, 2005 ("Office Action"), Applicant responds as follows:

OCT 1 4 2005

PTO/SB/21 (09-04)	1/2/1
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TRANSMITTAL	Filing Date	October 23, 2003			
FORM	First Named Inventor	Boerstler et al.			
	Art Unit	2836 Ronald Leja			
(to be used for all correspondence after initial filing)	Examiner Name				
Total Number of Pages in This Submission 8	Attorney Docket Number	AUS920030369US1 (IBM 2715000)			

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ENCLOSURES (Check all that apply)									
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Certified Copy of Priority Document(s)  Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53  Reply to Final Office Action dated August 10, 20  SIGNATURE OF APPLICANT, ATTORNEY, C								·	
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.